

<b>Notice of References Cited</b>	Application/Control No. 09/753,254	Applicant(s)/Patent Under Reexamination HELDER ET AL.	
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**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,571,385	05-2003	Muthukumar et al.	717/150
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
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	S					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Parallelization of Loops with Exits on Pipelined Architectures. P. Trimumalai et al, IEEE, 1990
	V	"Code Generation Schema for Modulo Scheduled Loops", B. Ramakrishn Rau et al, ACM, 1992
	W	"Unrolling-Based Optimizations for Modulo-Scheduling". Daniel M. Lavery et al, ACM, 1995
	X	"Software Pipelining Loops with Conditional Branches", Mark G. Stoodley et al, ACM, 1996

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	U	"Register Allocation for Software Pipelined Loops", B.R. Rau et al, ACM, 1992
	V	Non-unimodular Transformations of Nested Loops", J. Ramanujam, IEEE, 1992
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